

Listing of Claims:

1. (Original) A device for converting a digital pixel signal to an analog output signal having a target format, the device comprising:

a pixel pipeline circuit configured to provide a pixel stream comprising digital pixel values;

an encoder coupled to an output of the pixel pipeline circuit and configured to convert the pixel stream to digital sample values for a target analog signal representing the pixel stream in the target format, thereby generating a base data stream at a base sampling rate;

a supersampling circuit coupled to an output of the encoder and configured to generate a supersampled data stream at a supersampling rate from the base data stream, the supersampling rate being higher than the base sampling rate; and

a digital to analog converter coupled to an output of the supersampling circuit and configured to convert the supersampled data stream to an analog output signal.

2. (Original) The device of claim 1, wherein the supersampling rate is selected so as to provide substantial attenuation of a higher frequency echo in the analog output signal, the higher frequency echo occurring in a frequency band above a baseband of the analog output signal.

3. (Original) The device of claim 2, further comprising an electromagnetic interference (EMI) filter coupled to an output of the digital to analog converter and configured to substantially attenuate frequency components of the analog output signal above a maximum frequency.

4. (Original) The device of claim 3, wherein the supersampling rate is selected so as to substantially attenuate an echo of the analog output signal, the echo appearing in a frequency band between a baseband of the analog signal and the maximum frequency.

5. (Original) The device of claim 2, wherein the baseband of the analog output signal is determined with reference to a baseband for a standard definition television monitor.

6. (Original) The device of claim 2, wherein the baseband of the analog output signal is determined with reference to a baseband for a high definition television monitor.

7. (Original) The device of claim 1, wherein the encoder is further configured to respond to one or more control parameters, thereby enabling selection of one of a plurality of candidate formats as the target format.

8. (Original) The device of claim 7, wherein the plurality of candidate formats includes a standard definition television format and a high definition television format.

9. (Original) The device of claim 1, wherein the supersampling rate is substantially equal to twice the base sampling rate.

10. (Original) The device of claim 1, wherein the supersampling rate is substantially equal to four times the base sampling rate.

11. (Original) A device for converting a digital pixel signal to an analog output signal having a target format, the device comprising:

a pixel pipeline circuit configured to provide a pixel stream comprising a first number of digital pixel values per line at a base pixel rate;

a supersampling circuit coupled to an output of the pixel pipeline circuit and configured to generate a supersampled pixel stream comprising a second number of digital pixel values per line, the second number being greater than the first number, at a supersampling rate higher than the base pixel rate;

an encoder coupled to an output of the supersampling circuit and configured to convert the supersampled pixel stream to digital sample values for a target analog signal representing the supersampled pixel stream in the target format, thereby generating a supersampled data stream at an enhanced sampling rate; and

a digital to analog converter coupled to an output of the encoder and configured to convert the supersampled data stream to an analog output signal.

12. (Original) The device of claim 11, wherein the supersampling rate is selected so as to provide substantial attenuation of a higher frequency echo of the analog output signal, the higher frequency echo occurring in a frequency band above a baseband of the analog output signal.

13. (Original) The device of claim 12, further comprising an electromagnetic interference (EMI) filter coupled to an output of the digital to analog converter and configured to substantially attenuate all frequencies of the analog output signal greater than a maximum frequency.

14. (Original) The device of claim 13, wherein the supersampling rate is selected so as to substantially attenuate an echo of the analog output signal, the echo appearing in a frequency band between a baseband of the analog signal and the maximum frequency.

15. (Original) The device of claim 11, wherein the encoder is further configured to respond to one or more control parameters, thereby enabling selection of one of a plurality of candidate formats as the target format.

16. (Original) The device of claim 15, wherein the plurality of candidate formats includes a standard definition television format and a high definition television format.

17. (Original) A video processing unit comprising:
a pixel generator circuit configured to generate and store pixel data for a frame of an image;

a pixel pipeline configured to retrieve the stored pixel data and to provide a pixel stream comprising digital pixel values at a base pixel rate;

an encoder coupled to an output of the pixel pipeline and configured to convert the pixel stream to digital sample values for a target analog signal representing the pixel stream in a target format, thereby generating a base data stream at a base sampling rate;

a supersampling circuit coupled to an output of the encoder and configured to generate a supersampled data stream at a supersampling rate from the base data stream, the supersampling rate being higher than the base sampling rate; and

a digital to analog converter coupled to an output of the supersampling circuit and configured to convert the supersampled data stream to an analog output signal,

wherein the supersampling rate is selected so as to provide substantial attenuation of a higher frequency echo in the analog output signal, the higher frequency echo occurring in a frequency band above a baseband of the analog output signal.

18. (Original) The video processing unit of claim 17, wherein the encoder is further configured to respond to one or more control parameters, thereby enabling selection of one of a plurality of candidate formats as the target format.

19. (Original) The device of claim 18, wherein the plurality of candidate formats includes a standard definition television format and a high definition television format.

20. (Original) A method for converting a digital pixel signal to an analog output signal having a target format, the method comprising:

receiving a pixel stream comprising digital pixel values;

encoding the pixel stream as a base data stream comprising digital sample values for a corresponding analog signal having the target format, wherein the encoding is performed at a base sampling rate;

supersampling the base data stream at a supersampling rate, the supersampling rate being higher than the base sampling rate, thereby generating a supersampled data stream; and

converting the supersampled data stream to an analog output signal,

wherein the supersampling rate is selected so as to provide substantial attenuation of a higher frequency echo in the analog output signal, the higher frequency echo occurring in a frequency band above a baseband of the analog output signal.

21. (Original) The method of claim 20, further comprising:
selecting the target format from a plurality of candidate formats.

22. (Original) The method of claim 21, wherein the plurality of candidate formats includes a standard definition television format and a high definition television format.